

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	28083	(single or one) near4 (hardware or chip or processor or DSP or DMA or micro or MCU or circuit) near4 (cycle or routine or step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 06:39
L2	642270	(combin\$6 or two or multiple or many or combinat\$6) with (logic or algorithm or DES or CHAIN or computat\$6 or calculat\$5 or iterat\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:00
L3	5802	L1 and L2 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:17
L4	3418	L3 and (register with (stor\$5 or keep\$5 or maintain\$5 or input\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 06:43
L5	2429	L4 and (register with (receiv\$5 or provid\$5 or get\$5 or sent\$5 or receiv\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 06:44
L6	279	L5 and DES and CHAIN	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 06:44
L7	209	L6 and key\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 06:55
L8	2	("6876657").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/06 06:57
L9	3	"single hardware cycle"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 06:59



L10	184525	(one or single) near3 cycle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:00
L11	310	(one or single) near3 hardware near3 cycle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:00
L12	238	L2 and L11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:00
L13	186	L12 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:16
L14	851	(crypto\$6 or encrypt\$5).ab. and (single or one).ab. and (hardware or circuit).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:17
L15	20217292	L14 @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:17
L16	489	L14 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:27
L17	0	L16 and register and DES and CHANIN\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:18
L18	114	L16 and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:26
L19	1	cominational.ab. and logic.ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:26



L20	1391	combinational.ab. and logic.ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:26
L24	1043	L20 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:28
L25	3	L24 and encrypt\$5 and decrypt\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:29
L26	6	L24 and encrypt\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:30
L27	1103	380/28.ccls. or 380/207.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:30
L28	7	L27 and (combinational near3 logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:36
L29	780	(encrypt\$5 and decrypt\$5).ab. and device.ab. and (single or one).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:36
L30	371	L29 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:36
L31	61	L30 and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:47
L32	8195	combinational near3 logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:48



L33	5019	L32 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:48
L34	157373	(single or one) near3 (hardware or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:49
L35	1054	L34 and L33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:49
L36	881	L35 and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:49
L37	35	L36 and encrypt\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:51
L38	1106	(integrated near3 circuit) with encrypt\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 07:51
L39	464	L38 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 08:04
L40	215	L39 and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 08:01
L41	1090	((IC or (integrated circuit)) with encrypt\$5) and ((IC or (integrated circuit)) with decrypt\$5) and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 08:02
L42	0	L41 and (block near3 cipher\$5) and algorith and key\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 08:03



L43	141	L41 and (block near3 cipher\$5) and algorithm and key\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 08:03
L44	79	L43 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:40
L45	2	"6408074"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 09:40
S1	6	((("6189095") or ("6351539") or ("6424714")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/06 06:38
S2	3	((("6189095") or ("6351539") or ("6424714")).PN.) and " DES "	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/08 08:01
S3	0	"6189095".URPN.	USPAT	OR	ON	2004/09/08 07:43
S4	1	"6351539".URPN.	USPAT	OR	ON	2004/09/08 07:43
S5	2	"6424714".URPN.	USPAT	OR	ON	2004/09/08 07:44
S6	2	("6351539".URPN. or "6424714".URPN.) and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/08 08:26
S7	2	("6671771").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/08 07:47
S8	1	"5673319".PN.	USPAT	OR	OFF	2004/09/08 07:54
S9	1	"5666414".PN.	USPAT	OR	OFF	2004/09/08 07:54
S10	1	"5548648".PN.	USPAT	OR	OFF	2004/09/08 07:54
S11	1	"5513262".PN.	USPAT	OR	OFF	2004/09/08 07:54
S12	12	"5513262".URPN.	USPAT	OR	ON	2004/09/08 07:54
S13	1	"5513262".PN.	USPAT	OR	OFF	2004/09/08 07:55
S14	1	"5481613".PN.	USPAT	OR	OFF	2004/09/08 07:55
S15	37	"5481613".URPN.	USPAT	OR	ON	2004/09/08 07:55



S87	193	(S84 or S85) and S86	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/18 06:06
S88	319	380/29.CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:55
S89	281	S88 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:09
S90	457073	hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 11:52
S91	81274	(first with register) and (second with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:03
S92	334391	DES and chain\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 11:54
S93	4	"6189095"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 11:55
S94	1671	S90 and S91 and S92	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 11:55
S95	1041	S94 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:03
S96	13777	"380"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 11:56



S97	60	S95 and S96 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 11:56
S98	163	(first with register) with (stor\$5 or keep\$5 or memory or maintain\$4) with encrypt\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:09
S99	18	S94 and S98	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:03
S10 0	9	S99 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:03
S10 1	9126	S96 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:16
S10 2	981	register with (stor\$5 or keep\$5 or memory or maintain\$4) with encrypt\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:24
S10 3	225	S101 and S102	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:10
S10 4	40	S92 and S103	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:13
S10 5	50656	(single or one) with (cycle or function or process) with (hardware or chip or EPROM or EEPROM or PROM or MCU or micro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:14
S10 6	64131	(single or one) with (cycle or function or process or iteration or step) with (hardware or chip or EPROM or EEPROM or PROM or MCU or micro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:22



S10 7	2899	S106 and S92	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:16
S10 8	38	S107 and S102	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:16
S10 9	17	S108 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:16
S11 0	7	("6708273").URPN.	USPAT	OR	ON	2006/01/05 12:20
S11 1	15707	(single or one) near4 (cycle or function or process or iteration or step) near4 (hardware or chip or EPROM or EEPROM or PROM or MCU or micro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:23
S11 2	469	S111 and register and DES and chain\$5 and algorithm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:23
S11 3	217	S112 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:23
S11 4	36	S113 and ((stor\$5 or keep\$5 or memory or maintain\$4) with encrypt\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:24
S11 5	49	DMA near4 (combinat\$5 or combin\$5 or two or more or many) near4 (algorithm or DES or chain\$5 or CBC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:00
S11 6	25	S115 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:01



S11 7	214234	(combinat\$5 or combin\$5 or two or more or many) near4 (algorithm or DES or chain\$5 or CBC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:55
S11 8	124856	S117 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:55
S11 9	319	380/29.CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:56
S12 0	106	S118 and S119	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:56
S12 1	0	("6408074").URPN.	USPAT	OR	ON	2006/01/05 12:58
S12 2	291	DSP near4 (combinat\$5 or combin\$5 or two or more or many) near4 (algorithm or DES or chain\$5 or CBC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:00
S12 3	111	S122 and @ad<"20010126"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:17
S12 4	981	register with (stor\$5 or keep\$5 or memory or maintain\$4) with encrypt\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:02
S12 5	28337	(stor\$5 or keep\$5 or memory or maintain\$4) with encrypt\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:02
S12 6	981	S124 and S125	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:03



S12 7	981	S126 and (register or input or "I/O")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:03
S12 8	0	S123 and S125	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:03
S12 9	4	"6434699"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 14:17



Day : Friday  
Date: 1/6/2006  
Time: 06:48:26

# **PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = CALVIGNAC

First Name = JEAN LOUIS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09210222</a>	<a href="#">6785278</a>	150	12/10/1998	METHODS, SYSTEMS AND COMPUTER PROGRAM PRODUCTS FOR HASHING ADDRESS VALUES	CALVIGNAC, JEAN LOUIS
<a href="#">09312148</a>	<a href="#">6298340</a>	150	05/14/1999	SYSTEM AND METHOD AND COMPUTER PROGRAM FOR FILTERING USING TREE STRUCTURE	CALVIGNAC, JEAN LOUIS
<a href="#">09374003</a>	<a href="#">6498781</a>	150	08/13/1999	SELF-TUNING LINK AGGREGATION SYSTEM	CALVIGNAC, JEAN LOUIS
<a href="#">09384689</a>	<a href="#">6766381</a>	150	08/27/1999	VLSI NETWORK PROCESSOR AND METHODS	CALVIGNAC, JEAN LOUIS
<a href="#">09384691</a>	<a href="#">6769033</a>	150	08/27/1999	NETWORK PROCESSOR PROCESSING COMPLEX AND METHODS	CALVIGNAC, JEAN LOUIS
<a href="#">09384692</a>	<a href="#">6985431</a>	150	08/27/1999	NETWORK SWITCH AND COMPONENTS AND METHOD OF OPERATION	CALVIGNAC, JEAN LOUIS
<a href="#">09384744</a>	<a href="#">6460120</a>	150	08/27/1999	NETWORK PROCESSOR, MEMORY ORGANIZATION AND METHODS	CALVIGNAC, JEAN LOUIS
<a href="#">09384747</a>	<a href="#">6404752</a>	150	08/27/1999	NETWORK SWITCH USING NETWORK PROCESSOR AND METHODS	CALVIGNAC, JEAN LOUIS
<a href="#">09385603</a>	<a href="#">6868082</a>	150	08/30/1999	NETWORK PROCESSOR INTERFACE FOR BUILDING SCALABLE SWITCHING SYSTEMS	CALVIGNAC, JEAN LOUIS
<a href="#">09405669</a>	<a href="#">6681364</a>	150	09/24/1999	CYCLIC REDUNDANCY CHECK FOR PARTITIONED FRAMES	CALVIGNAC, JEAN LOUIS
<a href="#">09477179</a>	<a href="#">6557053</a>	150	01/04/2000	QUEUE MANAGER FOR A BUFFER	CALVIGNAC, JEAN LOUIS
<a href="#">09477576</a>	<a href="#">6473838</a>	150	01/04/2000	DRAM DATA STORAGE AND MOVEMENT FOR NETWORK PROCESSORS	CALVIGNAC, JEAN LOUIS
<a href="#">09479026</a>	Not Issued	123	01/07/2000	METHOD AND SYSTEM FOR MAKING A FRAME ALTERATION IN A NETWORK PROCESSING SYSTEM	CALVIGNAC, JEAN LOUIS
<a href="#">09771472</a>	Not	71	01/26/2001	Single-cycle hardware implementation of	CALVIGNAC, JEAN



	Issued			crypto-function for high throughput crypto-processing	LOUIS
<u>10017171</u>	Not Issued	61	12/07/2001	Sequence-preserving multiprocessing system with multimode TDM buffer	CALVIGNAC, JEAN LOUIS

Inventor Search Completed: No Records to Display.

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<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	<input type="text" value="CALVIGNAC"/>	<input type="text" value="JEAN LOUIS"/>	

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# **PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = PEYRAVIAN

First Name = MOHAMMAD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08506934</a>	<a href="#">5754754</a>	150	07/26/1995	TRANSMISSION ORDER BASED SELECTIVE REPEAT DATA TRANSMISSION ERROR RECOVERY SYSTEM AND METHOD	PEYRAVIAN, MOHAMMAD
<a href="#">08528822</a>	<a href="#">5642421</a>	150	09/15/1995	ENCRYPTION OF LOW DATA CONTENT ATM CELLS	PEYRAVIAN, MOHAMMAD
<a href="#">08592903</a>	<a href="#">5706348</a>	150	01/29/1996	USE OF MARKER PACKETS FOR SYNCHRONIZATION OF ENCRYPTION/DECRYPTION KEYS IN A DATA COMMUNICATION NETWORK	PEYRAVIAN, MOHAMMAD
<a href="#">08592931</a>	<a href="#">5805705</a>	150	01/29/1996	SYNCHRONIZATION OF ENCRYPTION/DECRYPTION KEYS IN A DATA COMMUNICATION NETWORK	PEYRAVIAN, MOHAMMAD
<a href="#">08725102</a>	<a href="#">5937066</a>	150	10/02/1996	TWO-PHASE CRYPTOGRAPHIC KEY RECOVERY SYSTEM	PEYRAVIAN, MOHAMMAD
<a href="#">08775348</a>	<a href="#">5907618</a>	250	01/03/1997	METHOD AND APPARATUS FOR VERIFIABLY PROVIDING KEY RECOVERY INFORMATION IN A CRYPTOGRAPHIC SYSTEM	PEYRAVIAN, MOHAMMAD
<a href="#">09181464</a>	<a href="#">6560337</a>	150	10/28/1998	SYSTEMS, METHODS AND COMPUTER PROGRAM PRODUCTS FOR REDUCING EFFECTIVE KEY LENGTH OF CIPHERS USING ONE- WAY CRYPTOGRAPHIC FUNCTIONS AND AN INITIAL KEY	PEYRAVIAN, MOHAMMAD
<a href="#">09181631</a>	<a href="#">6363154</a>	150	10/28/1998	DECENTRALIZED SYSTEMS METHODS AND COMPUTER PROGRAM PRODUCTS FOR SENDING SECURE MESSAGES AMONG A GROUP OF NODES	PEYRAVIAN, MOHAMMAD
<a href="#">09238700</a>	<a href="#">6507912</a>	150	01/27/1999	PROTECTION OF BIOMETRIC DATA VIA KEY-DEPENDENT SAMPLING	PEYRAVIAN, MOHAMMAD
<a href="#">09240120</a>	Not Issued	161	01/29/1999	METHOD AND SYSTEM FOR PROVIDING HASH-BASED ENCRYPTION	PEYRAVIAN, MOHAMMAD



<u>09248672</u>	Not Issued	161	02/11/1999	SYMMETRIC-KEY BLOCK ENCRYPTION SYSTEM	PEYRAVIAN, MOHAMMAD
<u>09262124</u>	6668323	150	03/03/1999	METHOD AND SYSTEM FOR PASSWORD PROTECTION OF A DATA PROCESSING SYSTEM THAT PERMIT A USER-SELECTED PASSWORD TO BE RECOVERED	PEYRAVIAN, MOHAMMAD
<u>09324308</u>	6940976	150	06/02/1999	GENERATING USER-DEPENDENT RSA KEYS	PEYRAVIAN, MOHAMMAD
<u>09324418</u>	6687375	150	06/02/1999	GENERATING USER-DEPENDENT KEYS AND RANDOM NUMBERS	PEYRAVIAN, MOHAMMAD
<u>09335378</u>	6697947	150	06/17/1999	BIOMETRIC BASED MULTI-PARTY AUTHENTICATION	PEYRAVIAN, MOHAMMAD
<u>09357483</u>	6928163	150	07/20/1999	METHODS, SYSTEMS AND COMPUTER PROGRAM PRODUCTS FOR GENERATING USER-DEPENDENT RSA VALUES WITHOUT STORING SEEDS	PEYRAVIAN, MOHAMMAD
<u>09458410</u>	Not Issued	41	12/10/1999	Time stamping method employing multiple receipts linked by a nonce	PEYRAVIAN, MOHAMMAD
<u>09458921</u>	Not Issued	61	12/10/1999	TIME STAMPING METHOD EMPLOYING A SEPARATE TICKET AND STUB	PEYRAVIAN, MOHAMMAD
<u>09458922</u>	Not Issued	61	12/10/1999	TIME STAMPING METHOD EMPLOYING USER SPECIFIED TIME	PEYRAVIAN, MOHAMMAD
<u>09458928</u>	Not Issued	95	12/10/1999	TIME STAMPING METHOD USING AGED TIME STAMP RECEIPTS	PEYRAVIAN, MOHAMMAD
<u>09458937</u>	6742119	150	12/10/1999	TIME STAMPING METHOD USING TIME DELTA IN KEY CERTIFICATE	PEYRAVIAN, MOHAMMAD
<u>09459187</u>	6965998	150	12/10/1999	TIME STAMPING METHOD USING TIME-BASED SIGNATURE KEY	PEYRAVIAN, MOHAMMAD
<u>09549944</u>	6826686	150	04/14/2000	METHOD AND APPARATUS FOR SECURE PASSWORD TRANSMISSION AND PASSWORD CHANGES	PEYRAVIAN, MOHAMMAD
<u>09642685</u>	Not Issued	95	08/21/2000	SECURE SOFTWARE DISTRIBUTION AND INSTALLATION	PEYRAVIAN, MOHAMMAD
<u>09642878</u>	Not Issued	95	08/21/2000	SECURE DATA STORAGE AND RETRIEVAL IN A CLIENT-SERVER ENVIRONMENT	PEYRAVIAN, MOHAMMAD
<u>09642879</u>	6947556	150	08/21/2000	SECURE DATA STORAGE AND RETRIEVAL WITH KEY MANAGEMENT AND USER AUTHENTICATION	PEYRAVIAN, MOHAMMAD
<u>09771472</u>	Not Issued	71	01/26/2001	Single-cycle hardware implementation of crypto-function for high throughput crypto-processing	PEYRAVIAN, MOHAMMAD
<u>09912781</u>	Not Issued	71	07/25/2001	Sequence-preserving deep-packet processing in a multiprocessor system	PEYRAVIAN, MOHAMMAD



<a href="#">09934886</a>	Not Issued	121	08/22/2001	Stateless message processing scheme for network processors interactions	PEYRAVIAN, MOHAMMAD
<a href="#">10017171</a>	Not Issued	61	12/07/2001	Sequence-preserving multiprocessing system with multimode TDM buffer	PEYRAVIAN, MOHAMMAD
<a href="#">10154308</a>	Not Issued	30	05/23/2002	Apparatus, method and computer program to reserve resources in communications system	PEYRAVIAN, MOHAMMAD
<a href="#">10361250</a>	Not Issued	30	02/10/2003	Method for distributing and authenticating public keys using hashed password protection	PEYRAVIAN, MOHAMMAD
<a href="#">10361433</a>	Not Issued	30	02/10/2003	Method for distributing and authenticating public keys using random numbers and Diffie-Hellman public keys	PEYRAVIAN, MOHAMMAD
<a href="#">10361515</a>	Not Issued	30	02/10/2003	Method for distributing and authenticating public keys using time ordered exchanges	PEYRAVIAN, MOHAMMAD
<a href="#">10464891</a>	Not Issued	95	06/19/2003	SYSTEM AND METHOD FOR ENCRYPTING AND VERIFYING MESSAGES USING THREE-PHASE ENCRYPTION	PEYRAVIAN, MOHAMMAD
<a href="#">10733713</a>	Not Issued	30	12/11/2003	Efficient method for providing secure remote access	PEYRAVIAN, MOHAMMAD
<a href="#">10733935</a>	Not Issued	30	12/10/2003	Methods for supplying cryptographic algorithm constants to a storage-constrained target	PEYRAVIAN, MOHAMMAD
<a href="#">10763079</a>	Not Issued	30	01/22/2004	Unidirectional message masking and validation system and method	PEYRAVIAN, MOHAMMAD
<a href="#">10918523</a>	Not Issued	20	08/12/2004	System, method and program to filter out login attempts by unauthorized entities	PEYRAVIAN, MOHAMMAD
<a href="#">10926598</a>	Not Issued	30	08/26/2004	Method and apparatus for efficient generation of modulo inverse for public key cryptosystems	PEYRAVIAN, MOHAMMAD
<a href="#">10937695</a>	Not Issued	20	09/09/2004	Front-end protocol for server protection	PEYRAVIAN, MOHAMMAD
<a href="#">10984299</a>	Not Issued	30	11/09/2004	Technique for detecting and blocking unwanted instant messages	PEYRAVIAN, MOHAMMAD
<a href="#">11021139</a>	Not Issued	20	12/23/2004	System and method for detecting keyboard logging	PEYRAVIAN, MOHAMMAD
<a href="#">11132535</a>	Not Issued	30	05/19/2005	System and method to record environmental condition on an RFID tag	PEYRAVIAN, MOHAMMAD
<a href="#">11176079</a>	Not Issued	30	07/07/2005	System and method for detection and mitigation of distributed denial of service attacks	PEYRAVIAN, MOHAMMAD
<a href="#">11283380</a>	Not Issued	20	11/17/2005	Applying blocking measures progressively to malicious network traffic	PEYRAVIAN, MOHAMMAD

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First Name = FABRICE JEAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09384689</a>	<a href="#">6766381</a>	150	08/27/1999	VLSI NETWORK PROCESSOR AND METHODS	VERPLANKEN, FABRICE JEAN
<a href="#">09384691</a>	<a href="#">6769033</a>	150	08/27/1999	NETWORK PROCESSOR PROCESSING COMPLEX AND METHODS	VERPLANKEN, FABRICE JEAN
<a href="#">09384692</a>	<a href="#">6985431</a>	150	08/27/1999	NETWORK SWITCH AND COMPONENTS AND METHOD OF OPERATION	VERPLANKEN, FABRICE JEAN
<a href="#">09384744</a>	<a href="#">6460120</a>	150	08/27/1999	NETWORK PROCESSOR, MEMORY ORGANIZATION AND METHODS	VERPLANKEN, FABRICE JEAN
<a href="#">09384747</a>	<a href="#">6404752</a>	150	08/27/1999	NETWORK SWITCH USING NETWORK PROCESSOR AND METHODS	VERPLANKEN, FABRICE JEAN
<a href="#">09385603</a>	<a href="#">6868082</a>	150	08/30/1999	NETWORK PROCESSOR INTERFACE FOR BUILDING SCALABLE SWITCHING SYSTEMS	VERPLANKEN, FABRICE JEAN
<a href="#">09405669</a>	<a href="#">6681364</a>	150	09/24/1999	CYCLIC REDUNDANCY CHECK FOR PARTITIONED FRAMES	VERPLANKEN, FABRICE JEAN
<a href="#">09477576</a>	<a href="#">6473838</a>	150	01/04/2000	DRAM DATA STORAGE AND MOVEMENT FOR NETWORK PROCESSORS	VERPLANKEN, FABRICE JEAN
<a href="#">09479026</a>	Not Issued	123	01/07/2000	METHOD AND SYSTEM FOR MAKING A FRAME ALTERATION IN A NETWORK PROCESSING SYSTEM	VERPLANKEN, FABRICE JEAN
<a href="#">09771472</a>	Not Issued	71	01/26/2001	Single-cycle hardware implementation of crypto-function for high throughput crypto-processing	VERPLANKEN, FABRICE JEAN
<a href="#">10017171</a>	Not Issued	61	12/07/2001	Sequence-preserving multiprocessing system with multimode TDM buffer	VERPLANKEN, FABRICE JEAN

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